

**WEST**

## Freeform Search

**Database:**

US Patents Full-Text Database  
US Pre-Grant Publication Full-Text Database  
JPO Abstracts Database  
EPO Abstracts Database  
Derwent World Patents Index  
IBM Technical Disclosure Bulletins

**Term:**

L20 and(sleep\$3 with (id or identifier or  
identification))

**Display:**  **Documents in Display Format:**  **Starting with Number** **Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Help

Logout

Interrupt

Main Menu

Show S Numbers

Edit S Numbers

Preferences

Cases

---

### Search History

---

**DATE:** Tuesday, July 29, 2003   [Printable Copy](#)   [Create Case](#)

**Set Name Query**

side by side

**Hit Count Set Name**

result set

*DB=USPT; PLUR=YES; OP=OR*

<u>L22</u>	L20 and(sleep\$3 with (id or identifier or identification))	23	<u>L22</u>
<u>L21</u>	L20 (sleep\$3 with (id or identifier or identification))	46541	<u>L21</u>
<u>L20</u>	l15 or l12	46294	<u>L20</u>
<u>L19</u>	L17 and ((id or identification) same ((processor or cpu) with (memory or register)))	1	<u>L19</u>
<u>L18</u>	L17 and ((processor or cpu) with (memory or register))	12	<u>L18</u>
<u>L17</u>	L16 or l14	23	<u>L17</u>
<u>L16</u>	L15 and smartcard	6	<u>L16</u>
<u>L15</u>	((365/\$)!.CCLS.)	33078	<u>L15</u>
<u>L14</u>	l12 and smartcard	18	<u>L14</u>
<u>L13</u>	l11 and smartcard	2	<u>L13</u>
<u>L12</u>	((711/\$)!.CCLS.)	14749	<u>L12</u>
<u>L11</u>	((711/170)!.CCLS. )	748	<u>L11</u>
<u>L10</u>	L7 and ((id or identification) adj2 circuit)	1	<u>L10</u>
<u>L9</u>	L7 and ((ic or (integrated adj circuit)) with(processor or cpu) with (memory or register) )	11	<u>L9</u>
<u>L8</u>	L7 and ((ic or (integrated adj circuit)) same (processor or cpu) same (memory or register) same (id or (identifier or identification) or (production adj3 identif\$5)))	3	<u>L8</u>
<u>L7</u>	((365/52)!.CCLS. )	326	<u>L7</u>
<u>L6</u>	L5	1	<u>L6</u>

*DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR*

<u>L5</u>	(shinsuke with moriai)	33	<u>L5</u>
<u>L4</u>	L3	1	<u>L4</u>

*DB=USPT; PLUR=YES; OP=OR*

<u>L3</u>	(katsuji with hosoda) and (integrated with circuit)	1	<u>L3</u>
<u>L2</u>	L1	141	<u>L2</u>

*DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR*

<u>L1</u>	katsuji and (integrated with circuit)	263	<u>L1</u>
-----------	---------------------------------------	-----	-----------

END OF SEARCH HISTORY